

FIG. 1 is a block diagram of a video processing system. The system includes a plurality of cameras (1-1, 1-2, ..., 1-N) connected to a central processing unit (3). Each camera outputs a video signal (VIDEO SIGNAL 1, VIDEO SIGNAL 2, ..., VIDEO SIGNAL N) to a corresponding capture board (2-1, 2-2, ..., 2-N). The capture boards are connected to a common bus (3), which is also connected to a processor (4), work memory (5), and a display board (6). The display board is connected to a display (8). A dictionary (7) is also connected to the system.

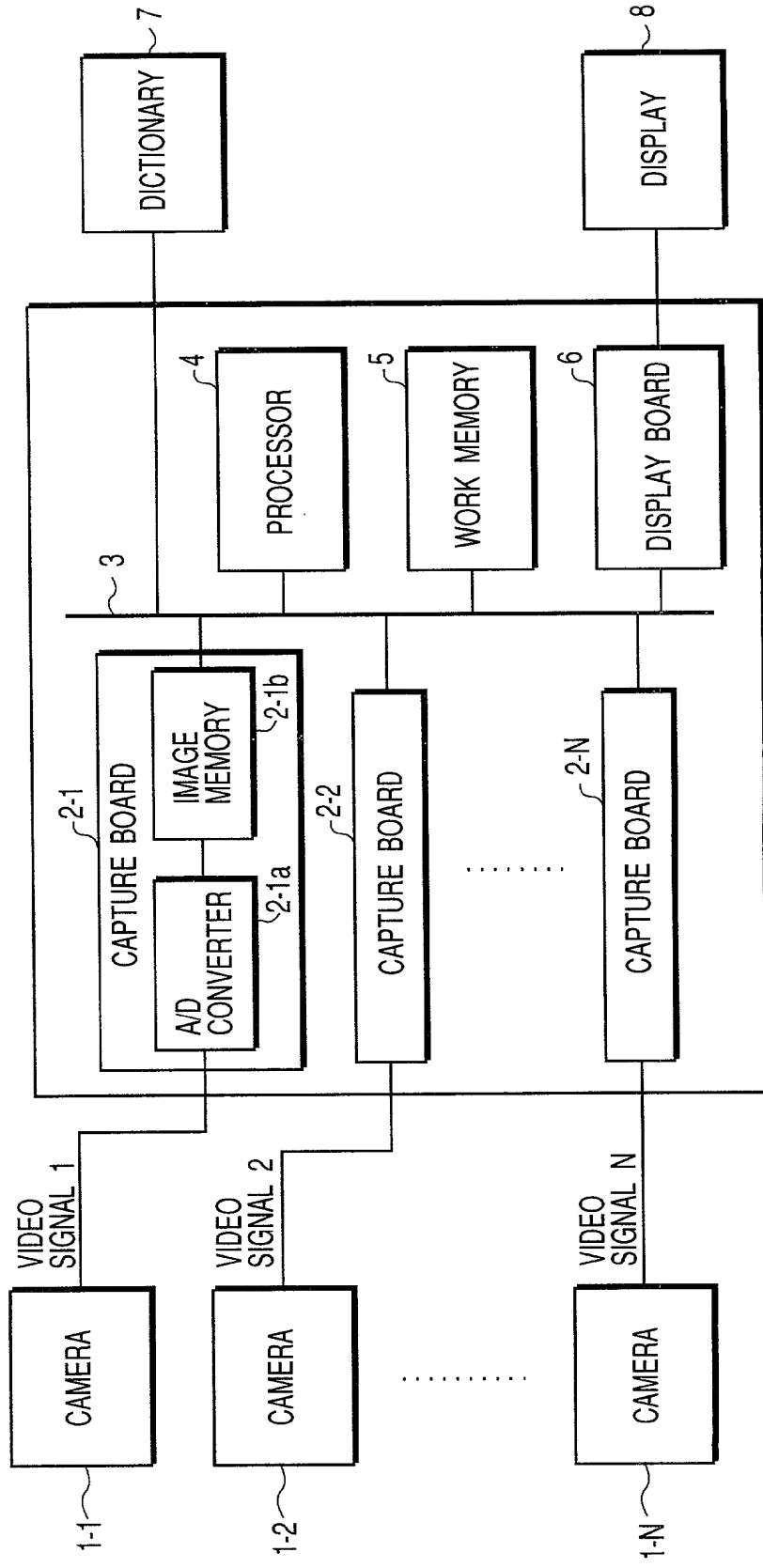


FIG. 1

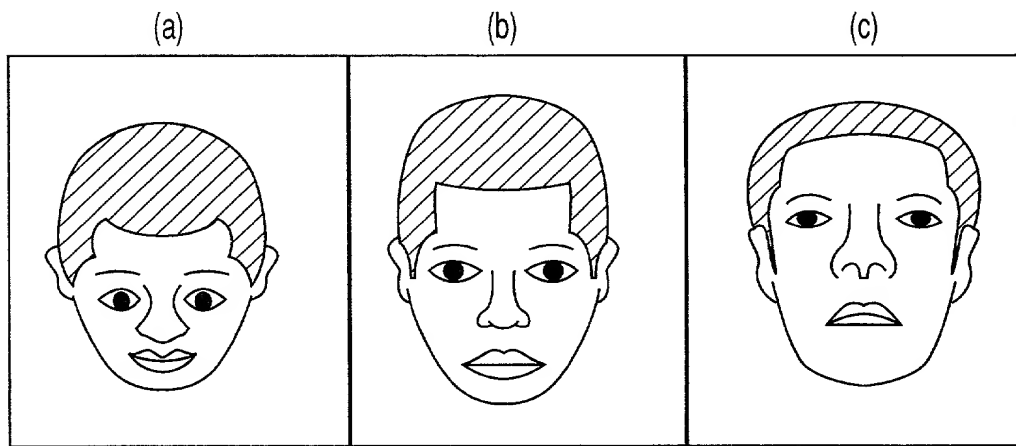
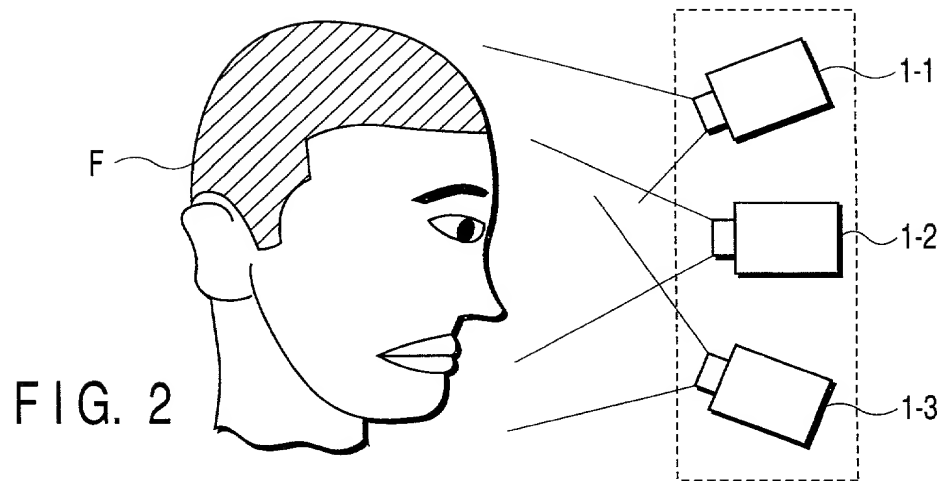
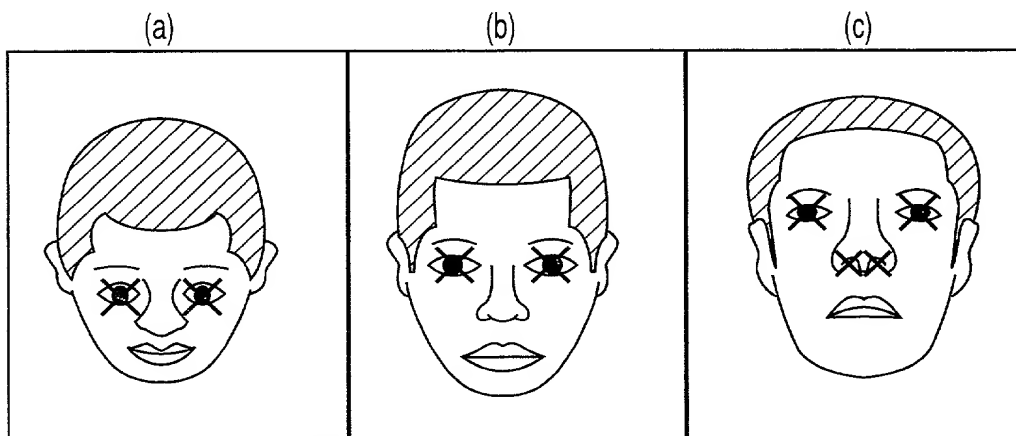


FIG. 3



X: FEATURE POINT

FIG. 5

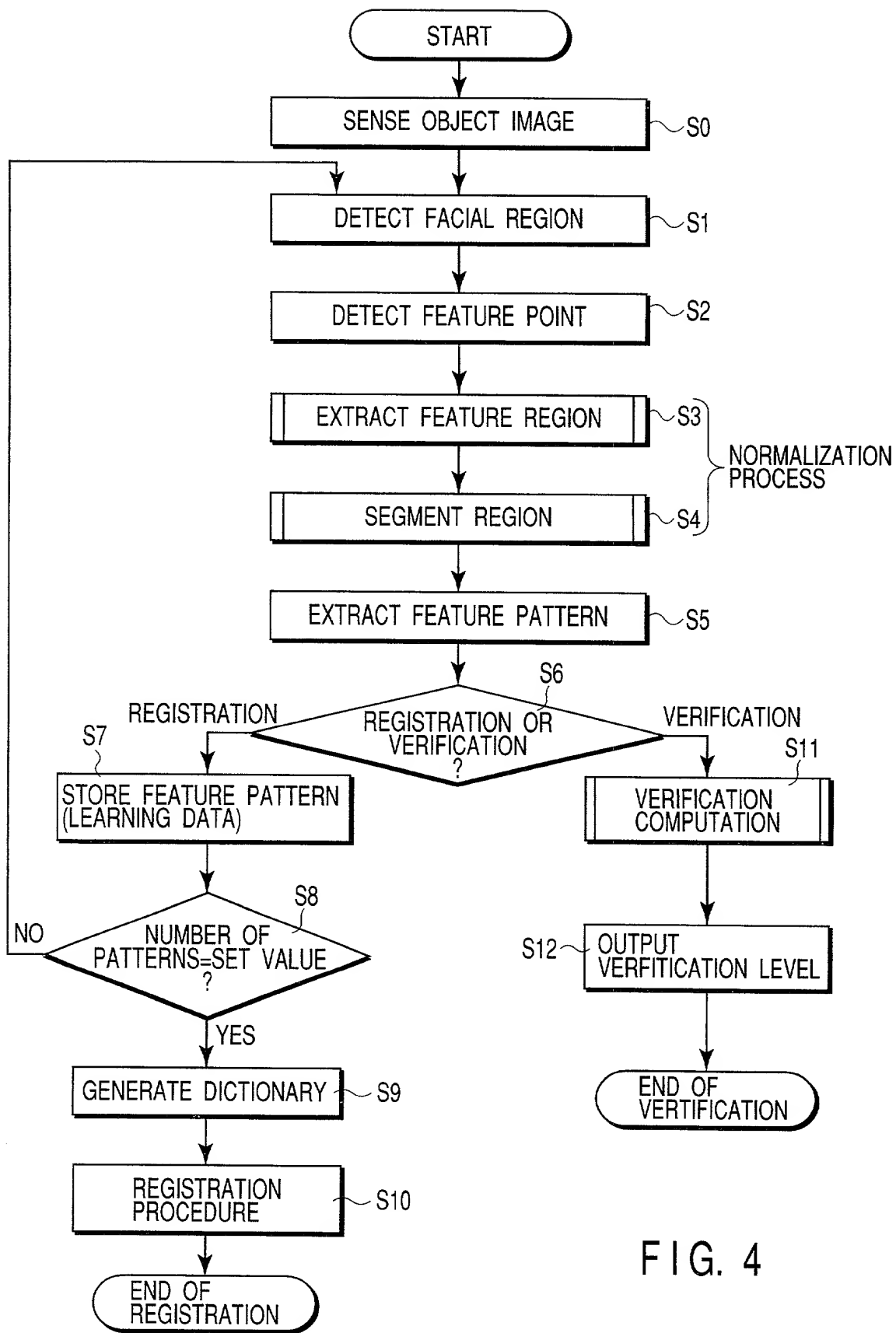


FIG. 4

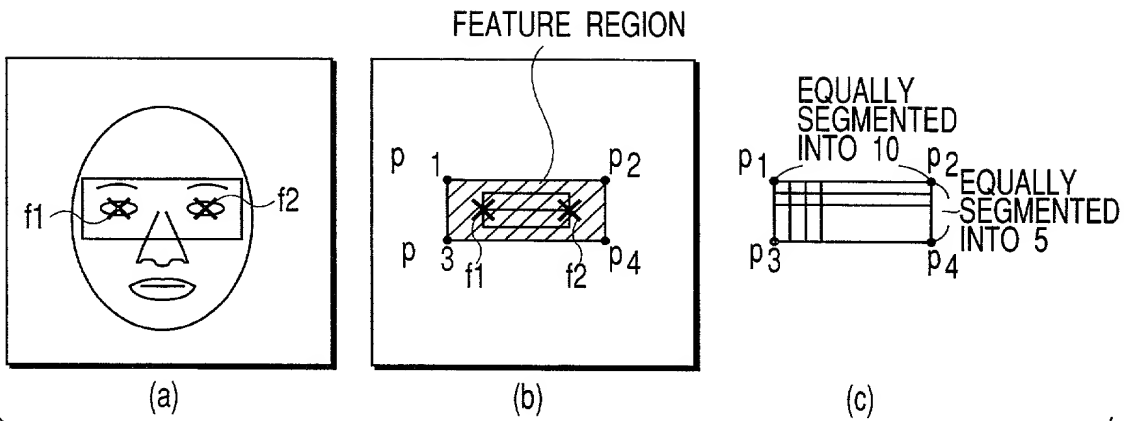


FIG. 6

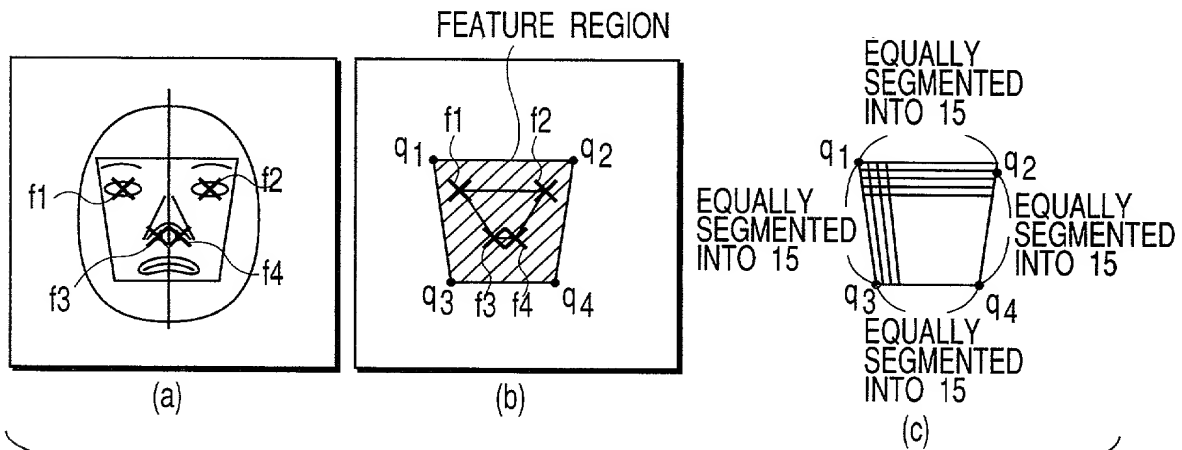
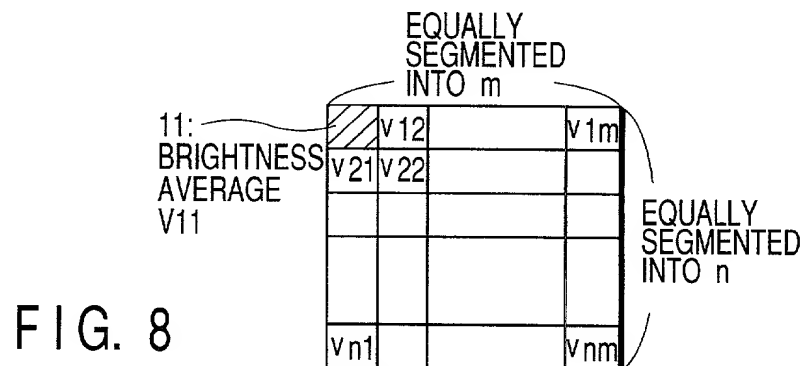


FIG. 7



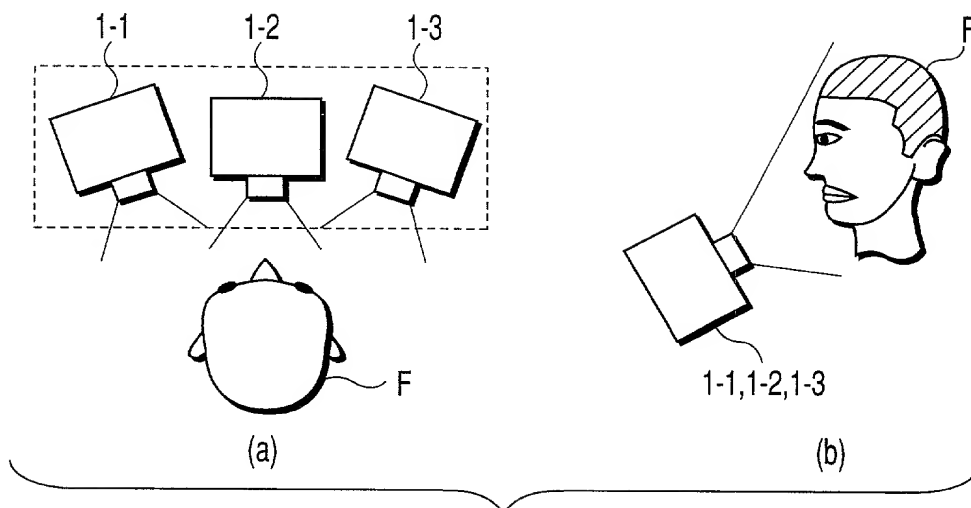


FIG. 9

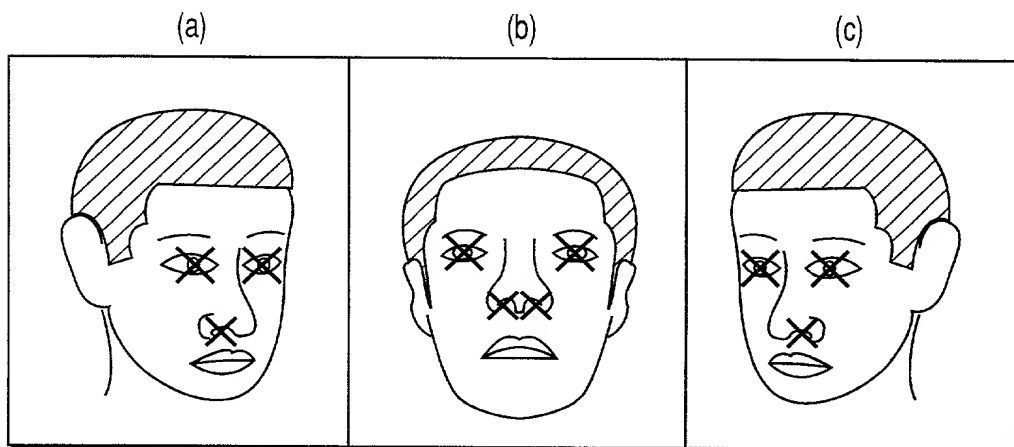


FIG. 10

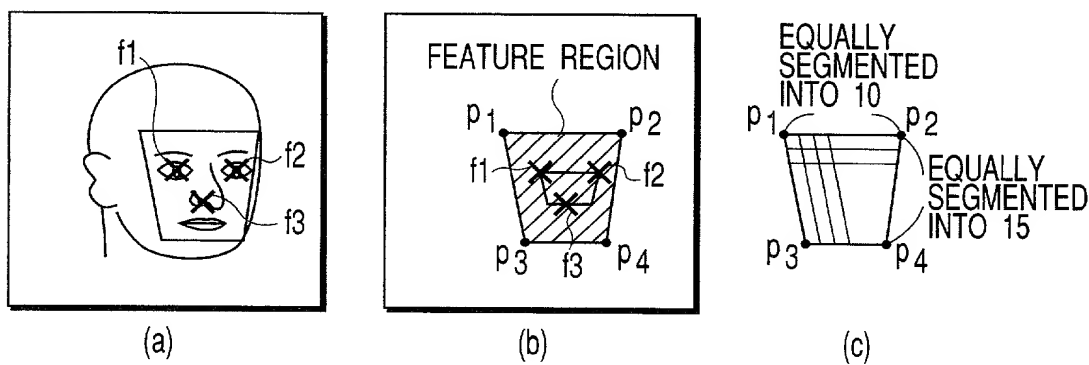


FIG. 11

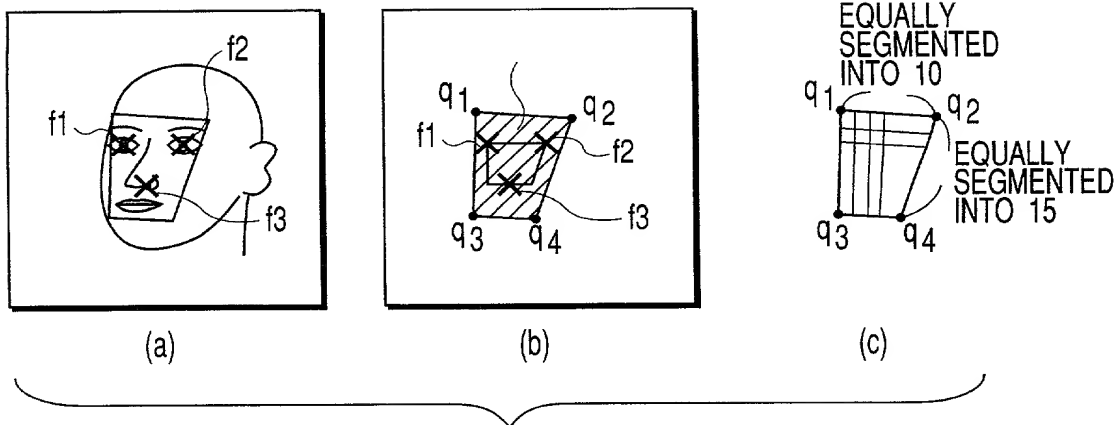


FIG. 12

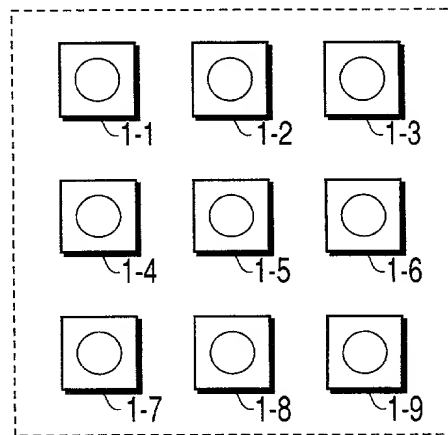


FIG. 13

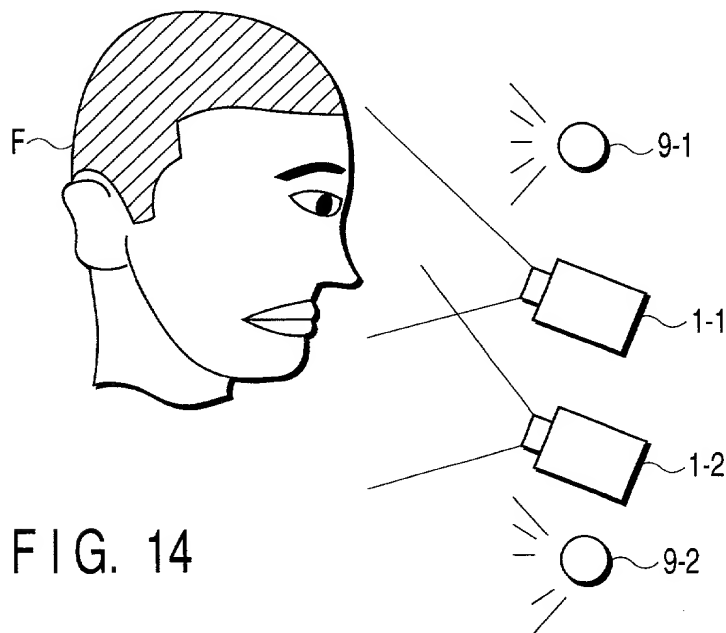


FIG. 14

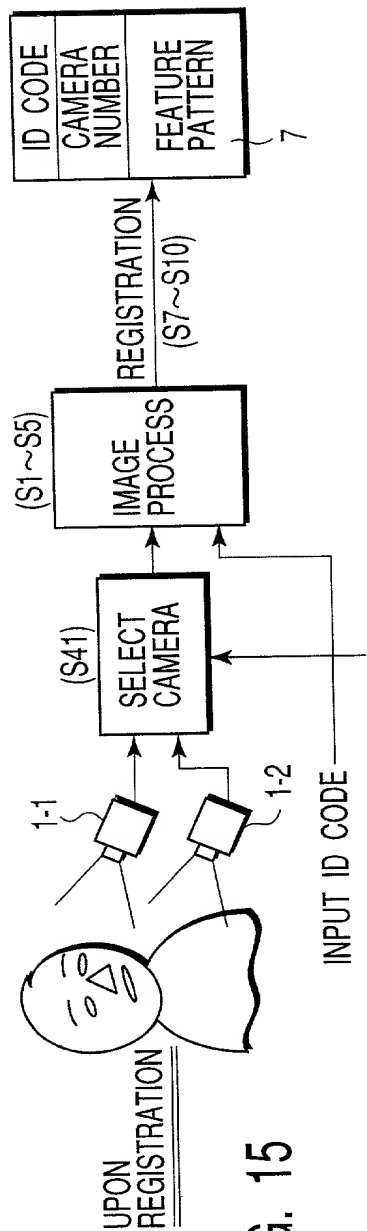


FIG. 15

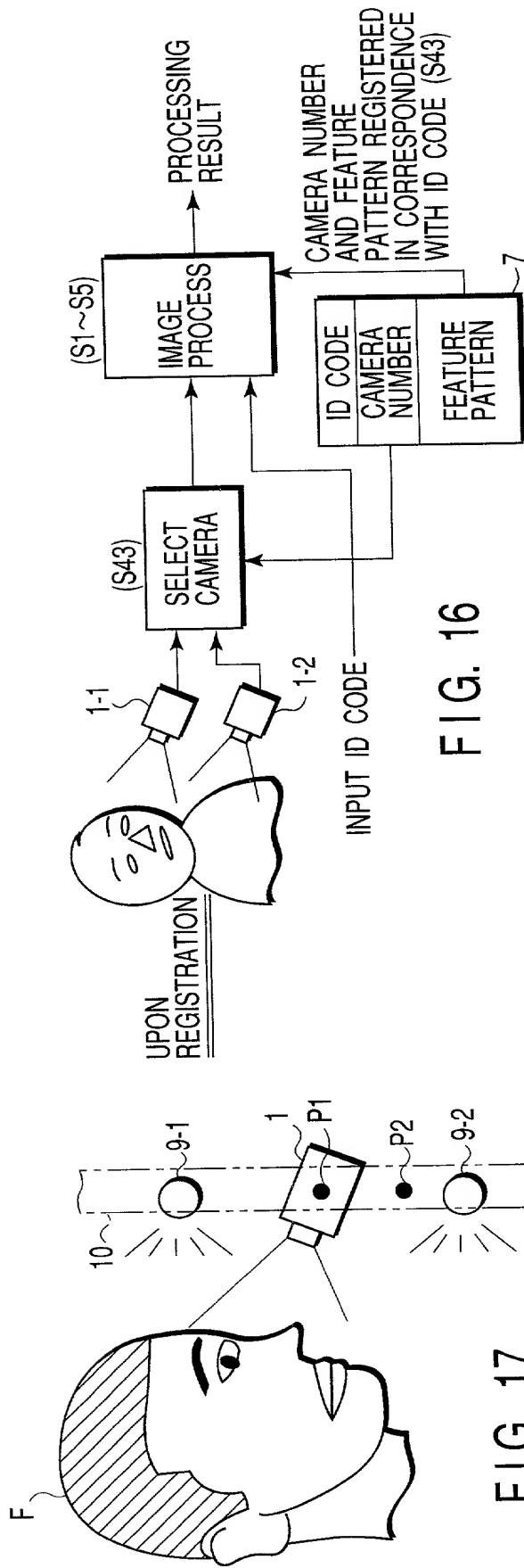


FIG. 16

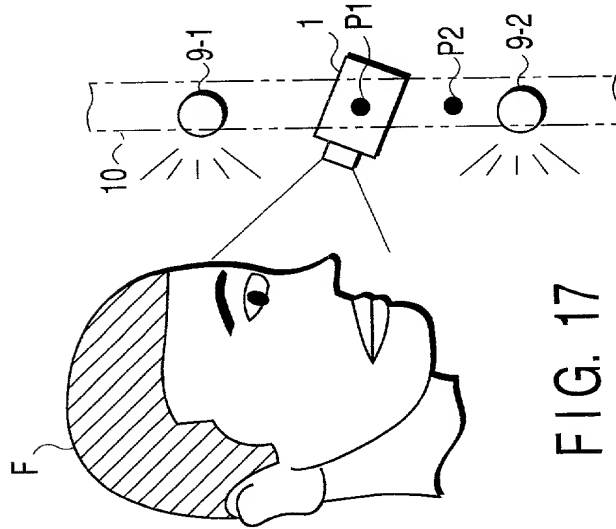


FIG. 17

FIG. 18 is a block diagram of a system 100. The system 100 includes a camera 1, a camera position controller 12, a capture board 2, a processor 4, work memory 5, a display board 6, a dictionary 7, and a display 8. The camera 1 is connected to the capture board 2. The camera position controller 12 is connected to the camera 1. The capture board 2 includes an A/D converter 2a and image memory 2b. The capture board 2 is connected to the processor 4. The processor 4 is connected to the work memory 5. The work memory 5 is connected to the display board 6. The display board 6 is connected to the dictionary 7. The display board 6 is also connected to the display 8.

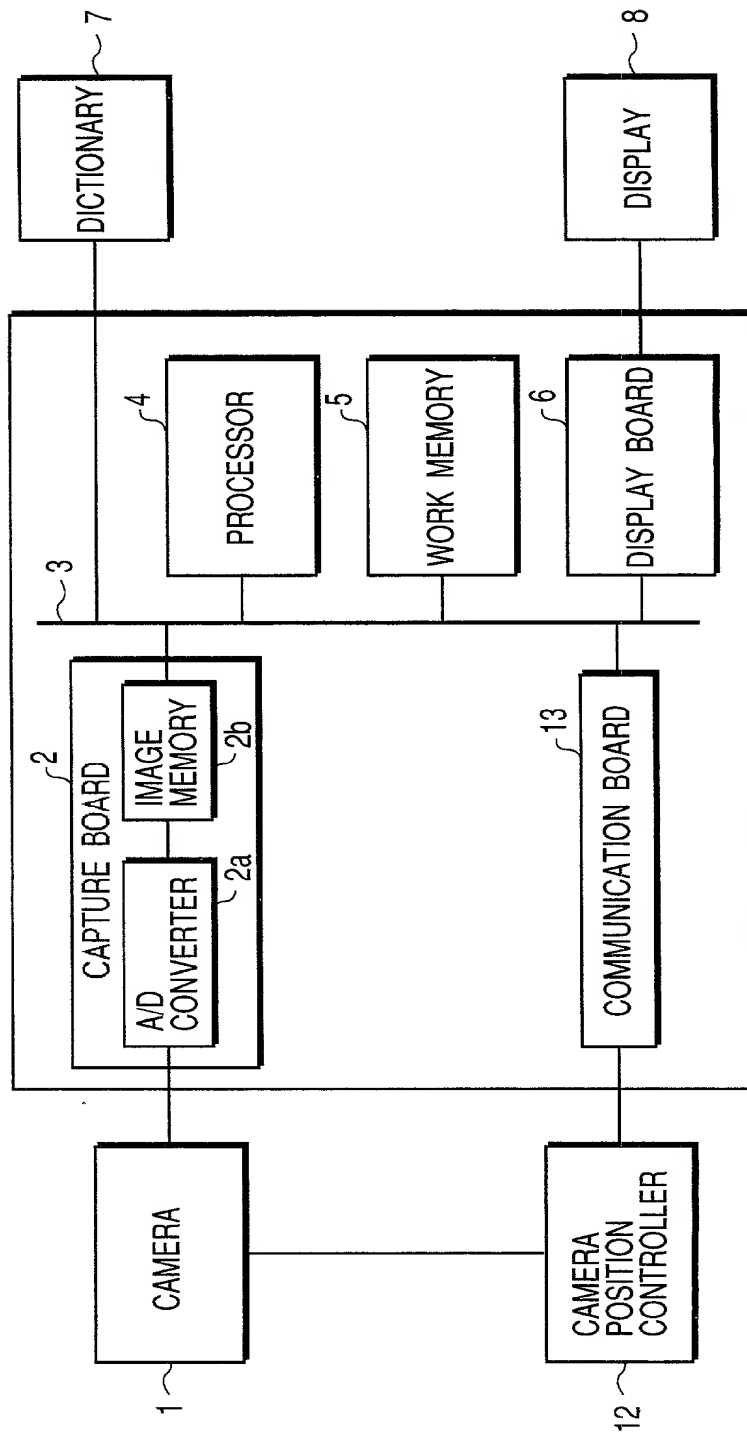


FIG. 18



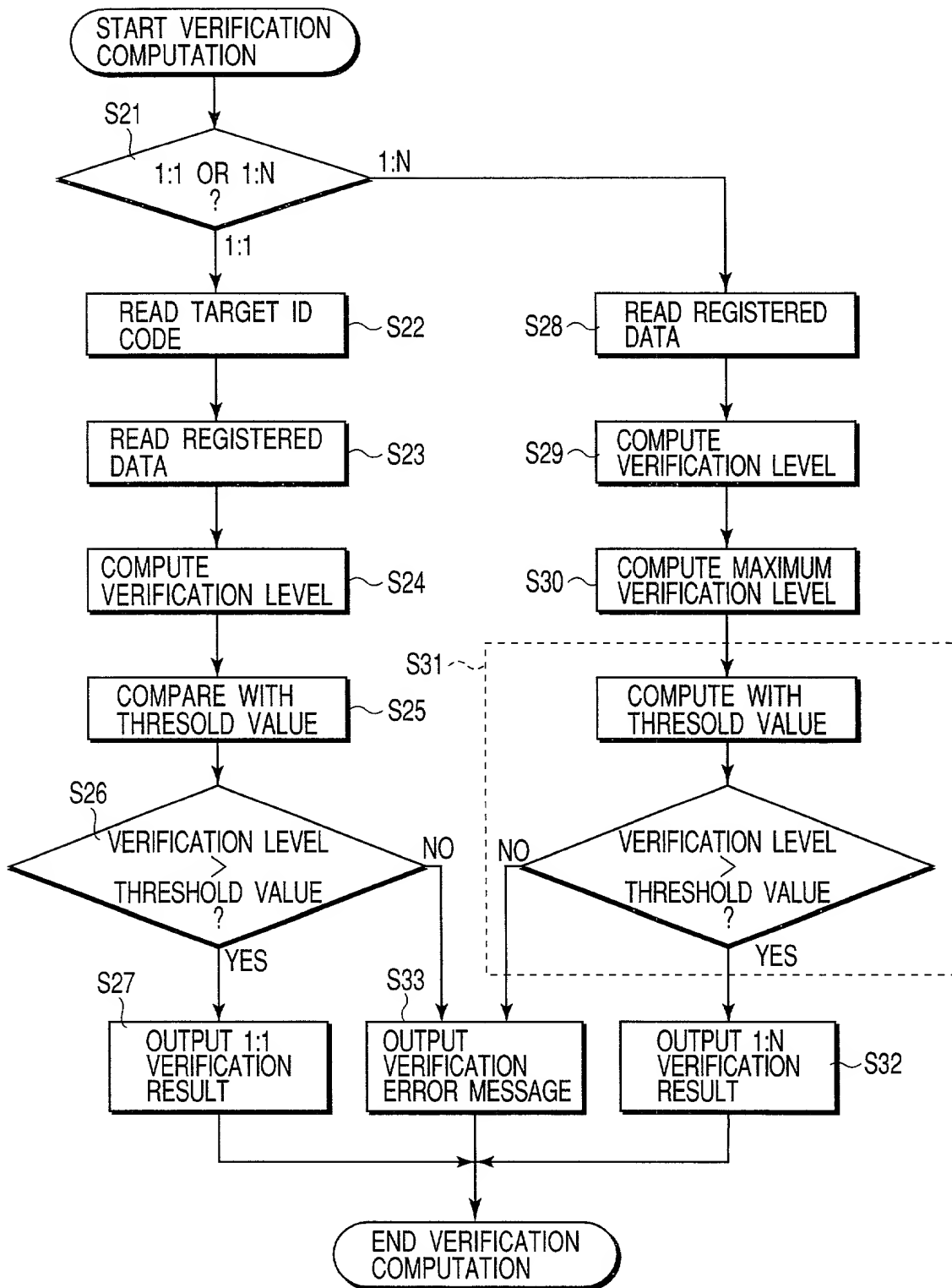


FIG. 19